

ICS 20
Logic Gates – AND, OR, NOT Gates

1. Draw the symbol and complete the truth table for the following logic gates

a. AND

Input		Output	Symbol
A	B		

b. OR

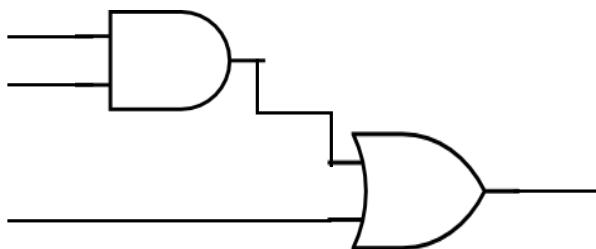
Input		Output	Symbol
A	B		

c. NOT

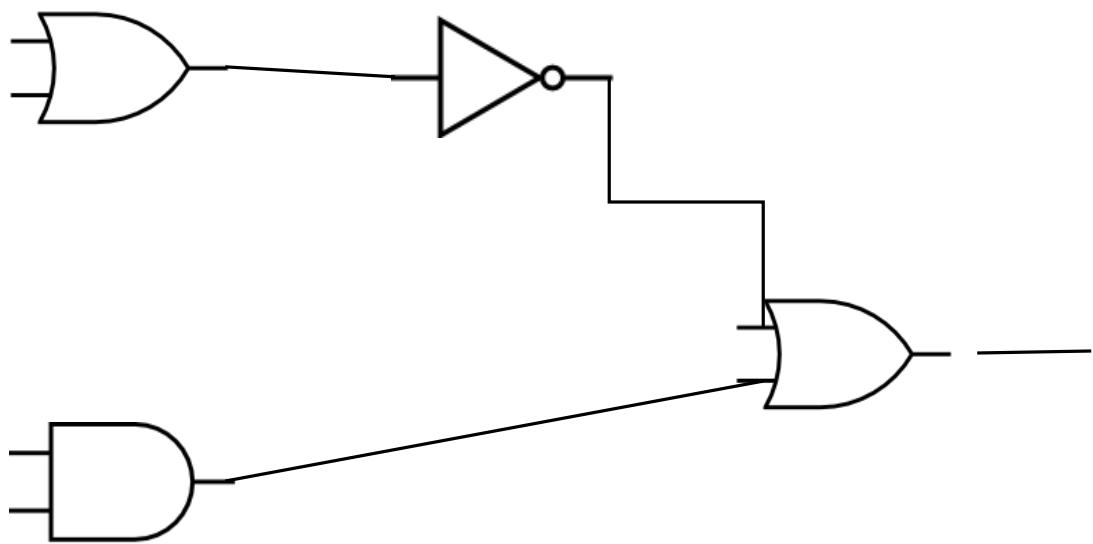
Input	Output	Symbol

2. What is the output of the following circuits?

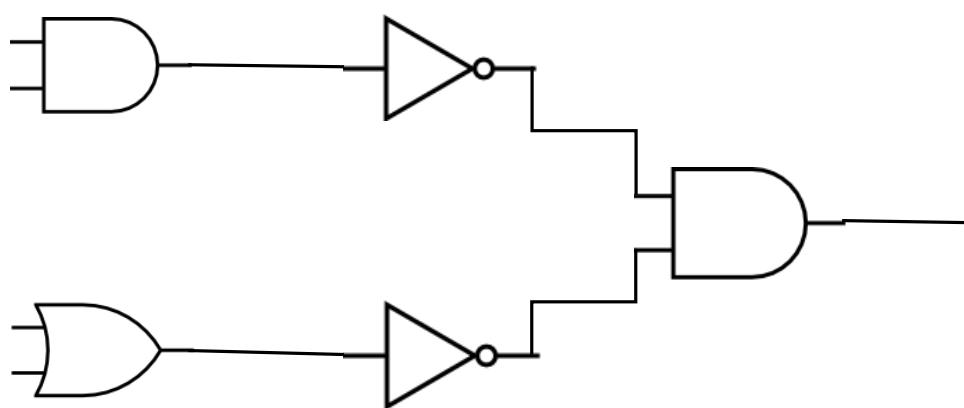
a.



b.

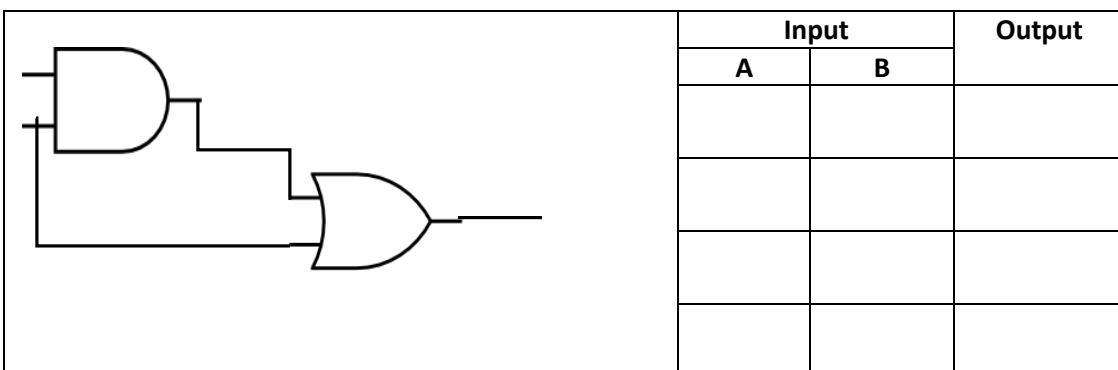


c.

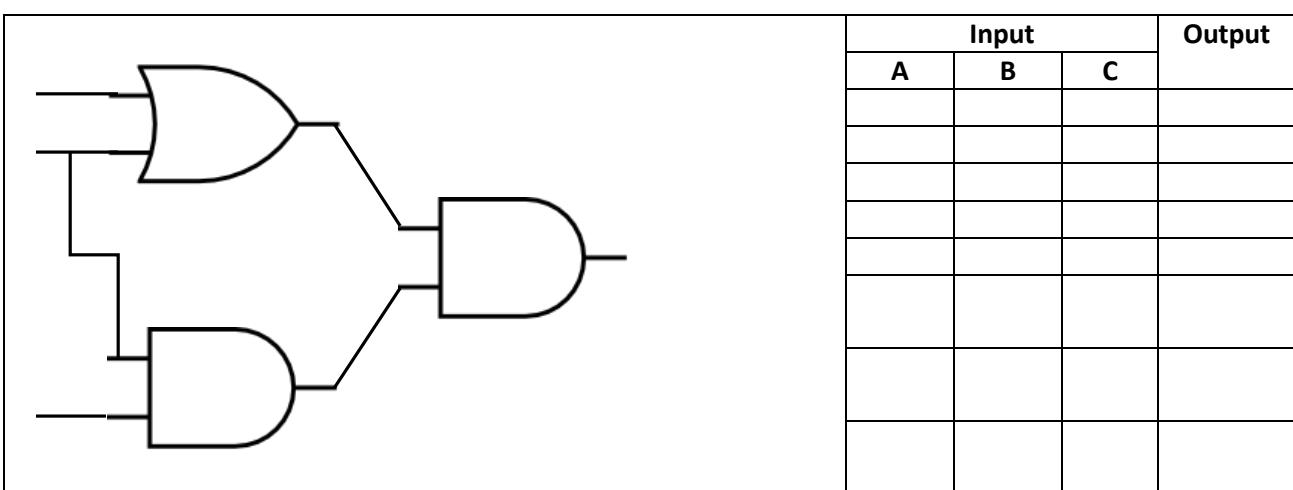


3. Complete the truth tables of the following circuits.

a.



b.



c.

